



10/636038

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Gurtej S. Sandhu

Examiner: Bret P Chen

Patent No.: 7,258,895

Group Art Unit: 1762

Issue Date: August 21, 2007

Docket No: 2002-0977.00/US

Title: METHODS OF FORMING MATERIAL ON A SUBSTRATE, AND A METHOD OF FORMING A
FIELD EFFECT TRANSISTOR GATE OXIDE ON A SUBSTRATE

ATTN: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

We are transmitting herewith the attached:

Request for Certificate of Correction.
 Certificate of Correction Form - PTO-1050 (in duplicate)
 A return postcard.

Please charge any additional fees or credit overpayment to Deposit Account No. 13-3092.

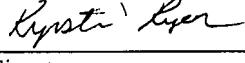
Customer Number: 26809

Respectfully submitted,

Date: September 11, 2007

By: 
Russ Slifer
Reg. No. 39,838

Kyrstin Ryan
Name


Signature

SEP 17 2007

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O I P E Title: METHODS OF FORMING MATERIAL ON A SUBSTRATE, AND A METHOD OF
APTO FORMING A FIELD EFFECT TRANSISTOR GATE OXIDE ON A SUBSTRATE



REQUEST FOR CERTIFICATION OF CORRECTION

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Alexandria, VA 22313-1450

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

As the error is that of the Patent Office, it is believed that no fee is due.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

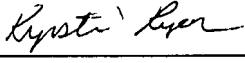
Customer Number: 26809

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Reg. No. 39,838

CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: ATTN: Certificate of Correction Branch, Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on this 11 day of September 2007.

Kyrstin Ryan
Name


Signature

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,258,895

Page (1) of 1

DATED : August 21, 2007

INVENTOR(S) : Sandhu

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (56), under "Other Publications", in column 2, line 6, delete "Microleectronic" and insert - - Microelectronic - -, therefor.

In column 9, line 4, in Claim 34, delete "TIN" and insert - - TiN - -, therefor.

SEP 17 2007

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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